LABORATOIRE D'ELECTROTECHNIQUE ET D'ELECTRONIQUE DE PUISSANCE DE LILLE





Master project, 2022-2023

Comparison of Power Transistor Technologies for Battery Chargers onboard Electric Vehicles

Supervisor : Arnaud VIDET, L2EP – Univ. Lille Email: <u>arnaud.videt@univ-lille.fr</u>

Context

Electrical energy is managed by power electronics converters in a huge amount of applications from daily-life home appliances to high-power industrial systems. Among them, e-mobility is a sector in heavy need for power electronics [1] due to their many conversions internally involved, such as electric powertrain (DC/AC), auxiliary loads (DC/DC notably), or battery charger (AC/DC). Figure 1 shows an example of various power conversion stages in an electrical vehicle. Among them, the on-board battery charger (OBC) typically ensures single-phase or three-phase AC conversion to the high-voltage battery, including a galvanic isolation stage that makes it a bulky and expensive device. In addition, power losses inside the converter require complex cooling system and impair efficiency. As a result, innovative OBC power electronics structures making use of the newest component technologies and special control techniques are needed. By achieving higher efficiency and power density, they will contribute to meet the high demand of electrical vehicles to improve the cost, available space, and energy consumption.

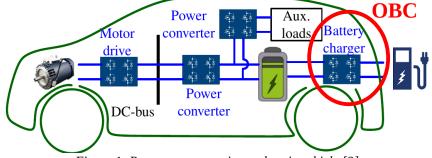


Figure 1: Power converters in an electric vehicle [3]

The Power Electronics team of the L2EP laboratory carries out a research project focusing on the design and control of high-efficiency and high-power-density OBC. The power devices technologies that operate at the core of the converter have been studied in-depth in previous research, notably regarding new-generation silicon carbide (SiC) and gallium nitride (GaN) components that generally offer superior performance over conventional silicon (Si) devices. Various works focused on the characterization [2-4], modeling [5-7], and implementation constraints [8] of GaN devices so as to fully benefit from their outstanding electrical performance and improve the converters energy efficiency and power density. Further, these devices are currently being used to build laboratory prototypes of integrated motor drives [9-11].

Objectives

This research project aims at evaluating the benefits of wide bandgap (WBG) power devices regarding especially the power losses in a three-phase AC to DC isolated power conversion structure suitable for OBC application.

SPICE software simulations scripted through a higher-level programming language will be used to compare the power losses of conventional and WBG devices. To handle large current with reduced losses, the paralleling technique will be considered, possibly taking into account the influence of self-heating. Parameter variations will be investigated regarding the power flow control at the AC network interface and the switching mode of the power transistors.

Based on the obtained results, an expected efficiency improvement of the converter will be quantified, demonstrating the performance of WBG devices and allowing to determing the optimal components to be implemented for a charger structure and control developed at L2EP.

Schedule

Motivated candidates apply by e-mail at <u>arnaud.videt@univ-lille.fr</u> to request an appointment. Latest transcripts should be provided. If the application is selected as part of the master thesis program, then preliminary works will be proposed in the first few months to gradually acquire specific knowledge on the subject. It includes a bibliographic study focusing on OBC structures and devices, and an intermediate scientific project related to simulations and estimation of losses. Consequently, the full-time internship in the second semester will take benefit from the former projects and continue toward the aforementioned objectives. The work will take place in the ESPRIT building of the University of Lille.

LABORATOIRE D'ELECTROTECHNIQUE ET D'ELECTRONIQUE DE PUISSANCE DE LILLE





References

- [1] *"Les priorités technologiques de la filière automobile et mobilités"*, Plateforme automobile (PFA) roadmap, pp. 4–5, retrieved 07/2020, url: https://pfa-auto.fr/wp-content/uploads/2020/07/feuille_de_route_pfa_version_finale_10h27.pdf
- [2] "Extraction of Packaged GaN Power Transistors Parasitics Using S-Parameters", L. Pace, N. Defrance, A. Videt, N. Idir, J-C Dejaeger, IEEE Transactions on Electron Devices, Vol. 66, N°. 6, pages. 2583-2588, 04/2019, <u>https://doi.org/10.1109/TED.2019.2909152</u>
- [3] "Experimental Investigation of GaN Transistor Current Collapse on Power Converter Efficiency for Electrical Vehicles", K. Li, A. Videt, N. Idir, P. Evans and M. Johnson, 2019 IEEE Vehicle Power and Propulsion Conference (VPPC), Hanoi, Vietnam, 10/2019, https://doi.org/10.1109/VPPC46532.2019.8952479
- [4] "Accurate Measurement of Dynamic on-State Resistances of GaN Devices Under Reverse and Forward Conduction in High Frequency Power Converter", K. Li, A. Videt, N. Idir, P. L. Evans and C. M. Johnson, IEEE Transactions on Power Electronics, vol. 35, no. 9, pp. 9650-9660, Sept. 2020, https://doi.org/10.1109/TPEL.2019.2961604
- [5] "Modelling GaN-HEMT Dynamic ON-state Resistance in High Frequency Power Converter", K. Li, A. Videt, N. Idir, P. Evans and M. Johnson, 2020 IEEE Applied Power Electronics Conference and Exposition (APEC), New Orleans, LA, USA, 03/2020, https://doi.org/10.1109/APEC39645.2020.9124513
- [6] "Electrothermal Modeling of GaN Power Transistor for High Frequency Power Converter Design", L. Pace, F. Chevalier, A. Videt, N. Defrance, N. Idir, J.-C. De Jaeger, 2020 22th European Conference on Power Electronics and Applications (EPE'20 ECCE Europe), Lyon, France, 09/2020
- [7] "A GaN-HEMT Compact Model Including Dynamic RDSon Effect for Power Electronics Converters", K. Li, P. Evans, M. Johnson, A. Videt and N. Idir, MDPI Energies, vol.14, no. 8, pages 17, Apr. 2021, <u>https://doi.org/10.3390/en14082092</u>
- [8] "Analysis of GaN Converter Circuit Stability Influenced by Current Collapse Effect", A. Videt, K. Li, N. Idir, P. Evans and M. Johnson, 2020 IEEE Applied Power Electronics Conference and Exposition (APEC), New Orleans, LA, USA, 03/2020, https://doi.org/10.1109/APEC39645.2020.9124351
- [9] "Integrated Smart Energy Converter", Projet CE2I, Contrat de Plan État-Région, http://ce2i.pole-medee.com
- [10] "Modeling and Experimental Analysis of a Single Leg towards the Design of an Integrated GaN Converter", S. Vienot, H. Hoffmann, A. Videt, T. Duquesne, N. Idir, 25th Symposium on Electromagnetic Phenomena in Nonlinear Circuits (EPNC), Arras, France, 06/2018
- [11] "Design of an Integrated GaN Inverter into a Multiphase PMSM", F. Salomez, S. Vienot, B. Zaidi, A. Videt, T. Duquesne, H. Pichon, E. Semail, N. Idir, submitted in 2020 IEEE Vehicle Power and Propulsion Conference (VPPC), Gijón, Spain, 10/2020